Se	arch	Notes	

Application/Control No.	Applicant(s)/Patent under Reexamination	_
10/702,325	FABRITIUS ET AL.	
Examiner	Art Unit	
Mohamed Charioui	2857	

	SEAR	CHED	
Class	Subclass	Date	Examiner
702	57-65, 119, 123	2/11/2005	мс
702	182-186	2/11/2005	МС
702	193	2/11/2005	мс
361	90	2/15/2005	мс
717	174	2/15/2005	МС
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INT	INTERFERENCE SEARCHED		
Class	Subclass	Date	Examiner
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SEARCH NOT (INCLUDING SEARCH		)
	DATE	EXMR
Consulted with Hal Wachsman	2/11/2005	МС
East: (analyz\$3 or controll\$3) with programmable with "run time"	2/13/2005	МС
East: ((FPGA or (field near5 programmable near5 gate near5 array)) with (run\$time or boot\$3)) and ((monitor\$3 or collect\$3)	2/13/2005	МС
near10 computer)	2/13/2005	МС
East: ((FPGA or (field near5 programmable near5 gate near5 array)) with run\$time) and (monitor\$3 or collect\$3)	2/15/2005	мс